



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2815

Examiner: Edward Joseph Wojciechowicz

RC file

In Re PATENT APPLICATION of:

Applicant(s): Katsuhiro HISAKA

Serial No.: 10/673,392

Filing Date: September 30, 2003

For: FUSE CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME

Atty. Dkt.: IIZ 138

)
)
) **PETITION FOR**
) **ONE-MONTH**
) **EXTENSION**
) **OF TIME**

October 26, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the period for replying to the Examiner's Action mailed on June 30, 2005, be extended for one month under the provisions of 37 C.F.R. 1.136(a).

The applicable extension fee of \$120.00 pursuant to 37 C.F.R. 1.17(a) is submitted herewith.

Should the remittance be accidentally missing or insufficient, the Commissioner is hereby authorized to charge the fee to our Deposit Account No. 18-0002, and advise us accordingly.

10/27/2005 SZEWDIE1 00000087 10673392

02 FC:1251

120.00 DP

October 26, 2005

Date

~~10/27/2005 SZEWDIE1 00000087 10673392~~

~~01 FC:1001~~

~~290.00 DP~~

Respectfully submitted,

Robert H. Berdo, Jr. – Registration No. 38,075
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RHB:vm